

The diagram shows a table with two main sections: 'generics' and 'constants'. The 'generics' section has columns for 'type' and 'value'. The 'constants' section has columns for 'type' and 'value'. Below these are 'ports' and 'signals' sections, each with 'type' and 'value' columns. The table is annotated with various numbers and arrows indicating specific elements and relationships.

generics			inst_1	inst_2
	type	value		
	{X	{Y		
constants				
	type	value		
ports				
	type	mode		
a	{bit	{in	e:in	
b	{integer	{in	f:in	
c	boolean	in		j:in
d	integer	out		l:out
signals				
	type	value		
si	integer		h:out	i:in
{sb	boolean		g:in	k:out

Annotations and arrows:

- 25 points to the 'generics' header.
- 17 points to the 'type' column of the 'generics' section.
- 19 points to the 'value' column of the 'generics' section.
- 21 points to the 'type' column of the 'constants' section.
- 22 points to the 'value' column of the 'constants' section.
- 29 points to the 'inst_1' header.
- 4 points to the 'inst_1' header.
- 26 points to the 'inst_2' header.
- 4 points to the 'inst_2' header.
- 24 points to the 'inst_2' header.
- 18 points to the first row of the 'generics' section.
- 14 points to the 'constants' header.
- 18 points to the first row of the 'constants' section.
- 20 points to the 'ports' header.
- 6 points to the first row of the 'ports' section.
- 27 points to the second row of the 'ports' section.
- 6 points to the third row of the 'ports' section.
- 5 points to the first row of the 'signals' section.
- 5 points to the second row of the 'signals' section.
- 31 points to the '{sb' entry in the 'signals' section.
- 28 points to the '{sb' entry in the 'signals' section.
- 12 points to the 'e:in' entry in the 'ports' section.
- 10 points to the 'j:in' entry in the 'ports' section.
- 9 points to the 'l:out' entry in the 'ports' section.
- 10 points to the 'i:in' entry in the 'signals' section.
- 7 points to the 'g:in' entry in the 'signals' section.
- 8 points to the 'k:out' entry in the 'signals' section.

Fig. 1

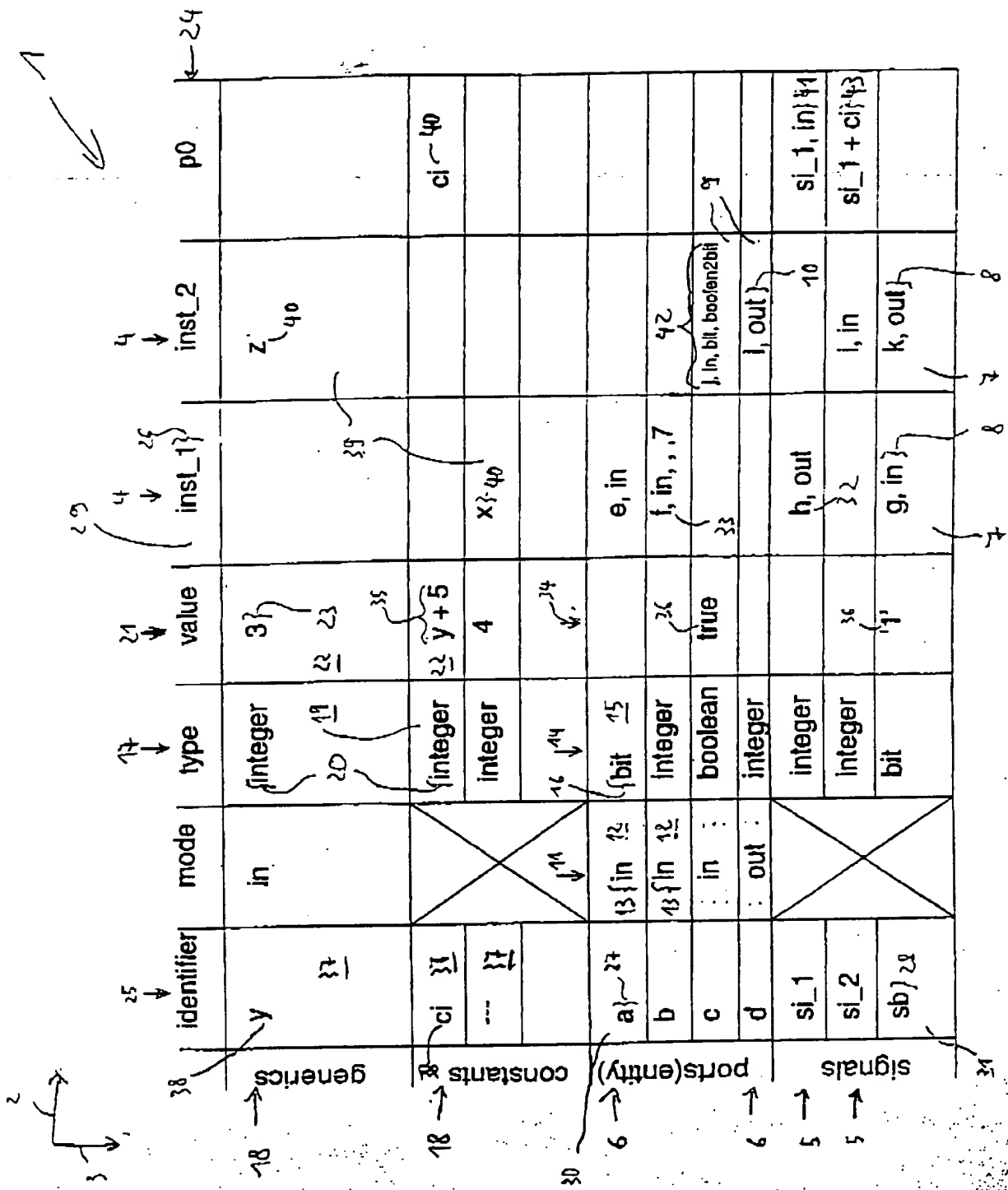


Fig. 2